**San Jose State University**

**Department of Computer Engineering**

**CMPE 140 Lab Report**

**Lab 8 Report**

**Title** Pipelined MIPS Processor & I/O Interface

**Semester** \_\_Fall 2018\_\_\_ **Date** \_\_11/20/2018\_\_\_\_\_

**by**

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**Lab Checkup Record**

| **Week** | **Performed**  **By**  **(signature)** | **Checked**  **By**  **(signature)** | **Tasks**  **Successfully**  **Completed\*** | **Tasks**  **Partially**  **Completed\*** | **Tasks Failed**  **or Not**  **Performed\*** |
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| **1** |  |  |  |  |  |
| **2** |  |  |  |  |  |
| **3** |  |  |  |  |  |
| **4** |  |  |  |  |  |